EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	1	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom) and ((delete deleted deleting adjust adjusting modify modifying) with table)) and (massage massager massaging vibrate vibrator vibrating)).clm. and ("128" "700" "601").clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:47
L8	35	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom) and ((delete deleted deleting adjust adjusting modify modifying) with table))).clm. and ("128" "700" "601").clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:47
L7	18	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom) and ((delete deleted deleting adjust adjusting modify modifying) with table))) and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:14
L6	0	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom) and ((delete deleted deleting adjust adjusting modify modifying) with table))) and (massage massager massaging vibrate vibrator vibrating) and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:14

L5	3	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom)) and (delete deleted deleting adjust adjusting modify modifying))) and (massage massager massaging vibrate vibrator vibrating) and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:13
L4	50	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom)) and (delete deleted deleting adjust adjusting modify modifying))).clm. and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:12
L3	40671	((controller microcontroller microprocessor processor microcomputer computer) and (memory (ram and rom)) and ((memory (ram rom)) and (delete deleted deleting adjust adjusting modify modifying))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:12
L2	1075	1 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/03/24 08:11
L1	1368	(700/86-88).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/03/24 08:11

3/24/2009 8:48:38 AM
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